

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	606	(cpu cpus (central adj processing adj units)) same (control near1 chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L2	173	((cpu cpus (central adj processing adj units)) same (control near1 chip)) and power and (ground grounded) and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L3	104	(((cpu cpus (central adj processing adj units)) same (control near1 chip)) and power and (ground grounded) and signal) and pins	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L4	23	(((cpu cpus (central adj processing adj units)) same (control near1 chip)) and power and (ground grounded) and signal) and pins) and pentium	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 10:15
L5	16	(((cpu cpus (central adj processing adj units)) same (control near1 chip)) and power and (ground grounded) and signal) and (layout near structure))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L6	4	((cpu cpus (central adj processing adj units)) same (control near1 chip)) and (power adj layer) and ((ground grounded)adj layer) and (signal adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L7	4	((cpu cpus (central adj processing adj units)) same (control near1 chip)) and (power adj layer) and (signal adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L8	1811	(cpu cpus (central adj processing adj unit)) and (control near1 chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L9	4	((cpu cpus (central adj processing adj unit)) and (control near1 chip)) and (power adj layer) and (signal adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L10	19	(cpu cpus (central adj processing adj unit)) and (north near bridge adj control)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L11	110	CHANG-NAI-SHUNG	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30

L12	35	CHANG-NAI-SHUNG and (pcb (printed adj circuit adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L13	631	700/1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L14	821	700/2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L15	285	700/5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L16	172	700/7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L17	1193	700/9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L18	500	700/19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L19	2946	700/1 700/2 700/5 700/7 700/9 700/19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L20	164280	(cpu cpus (central adj processing adj unit)) with control	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L21	520	(700/1 700/2 700/5 700/7 700/9 700/19) and ((cpu cpus (central adj processing adj unit)) with control)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L22	60	((700/1 700/2 700/5 700/7 700/9 700/19) and ((cpu cpus (central adj processing adj unit)) with control)) and power and (ground grounding grounded) and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30

L23	9	((700/1 700/2 700/5 700/7 700/9 700/19) and ((cpu cpus (central adj processing adj unit)) with control)) and power and (ground grounding grounded) and signal) and (pcb (printed adj circuit adj board) (wiring adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:31
L24	16	((700/1 700/2 700/5 700/7 700/9 700/19) and ((cpu cpus (central adj processing adj unit)) with control)) and power and (ground grounding grounded) and signal) and (pcb (printed adj circuit adj board) (wiring adj board) substrate)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L25	6	"6395996"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L26	2	"20030141585"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L27	1	"20030141585" and (first adj signal adj line)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L28	1	"20030141585" and (second adj signal adj line)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L29	2	"20030141585" and refer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L30	2	"20030141585"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L31	1	"20030141585" and (first adj side)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L32	39	"6011694"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 09:30
L33	4	("6011694" "6140707" "6153924" "6246111").PN.	USPAT	OR	ON	2004/12/27 09:30

L34	9	("4791075" "5216278" "5474957" "5602059" "5650662" "5663530" "5679978" "5710695" "5737191").PN.	USPAT	OR	ON	2004/12/27 09:30
L35	1	"6459045"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/27 09:38
L36	0	("6459045").URPN.	USPAT	OR	ON	2004/12/27 09:38
L37	6	("4498122" "4799128" "5191174" "5410107" "5841686" "6229095").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 09:38
L38	4	(US-20020156538-\$ or US-20030141585-\$).did. or (US-6395996-\$ or US-6459045-\$). did.	US-PGPUB; USPAT	OR	ON	2004/12/27 09:48
L39	32	"6198635"	US-PGPUB; USPAT	OR	ON	2004/12/27 09:49
L40	3008	174/255,261.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/27 10:14
L41	2189	361/760-761.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/27 10:14
L42	4765	40 41	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/27 10:14
L43	36	20 and 42	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 10:15
L44	25	43 and (method process)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 10:15
L45	2	44 and cutting	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/27 10:15